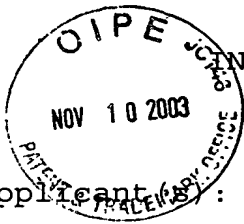


PATENT APPLICATION



IN THE U.S. PATENT AND TRADEMARK OFFICE

November 6, 2003

Applicant(s): Chikao IKENAGA et al.  
For : FRAME FOR SEMICONDUCTOR PACKAGE INCLUDING  
PLURAL LEAD FRAMES HAVING THIN PARTS OR  
HOLLOWS ADJACENT THE TERMINAL ROOTS

Serial No. : 09/850 213 Group: 2826

Confirmation No.: 9610

Filed : May 7, 2001 Examiner: P. Greene

Atty. Docket No.: OPS Case 533

Notice of Allowance Mailed: September 16, 2003

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

LETTER TRANSMITTING FORMAL DRAWINGS

Sir:

In response to the United States Patent and Trademark Office (USPTO) communication mailed on September 16, 2003, herewith are the formal drawings for the above-identified application. The formal drawings are believed identical in all respects to the informal drawings now present in the above-referenced file, except that amendments made during prosecution have been incorporated.

Please approve the enclosed formal drawings and then substitute them for the informal drawings now present in the USPTO file for this application. Please notify the undersigned in a timely manner if any corrections are required.

Respectfully submitted,

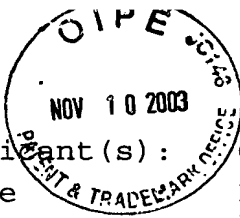
  
Liane L. Churney

LLC/jp

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	Liane L. Churney	Reg. No. 40 694
	Brian R. Tumm	Reg. No. 36 328
	Robert J. Sayfie	Reg. No. 37 714

Encl: Formal Drawings (7 sheets)  
Postal Card

166.05/03



## PATENT APPLICATION

Applicant(s): Chikao IKENAGA et al.

Title: FRAME FOR SEMICONDUCTOR PACKAGE INCLUDING  
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Alexandria, VA 22313-1450

## FIRST CLASS MAILING CERTIFICATE

Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service under 37 CFR 1.8 as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on November 6. 2003.

  
Liane L. Churney

LLC/jp

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Correspondence: Letter Transmitting Formal Drawings dated  
November 6, 2003  
Formal Drawings (7 sheets)  
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190.05/03